



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Koe, et al.

Art Unit: 2819

Serial No.: 10/766,233

Examiner: Mai, Lam T.

Filing Date: 01/28/04

Docket No.: TI-36585

Title: METHOD AND CIRCUIT FOR REDUCING QUANTIZER INPUT/OUTPUT SWING IN A SIGMA-DELTA MODULATOR

LETTER TO THE OFFICIAL DRAFTSPERSON

Assistant Commissioner for Patents
ATTN: Official Draftsperson
Washington, D.C. 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 on:

6-13-05



Sharlet Hurst

Dear Sir:

In response to the Notice of Allowance dated 03/15/2005 requiring formal drawings of the above identified application, please find enclosed eleven (11) sheets of formal drawings as required by the Examiner.

Respectfully submitted,


W. Daniel Swayze, Jr.
Reg. No. 34,478
Attorney for Applicants

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